

Tektronix TDS7404 Digital Phosphor Oscilloscope

Intro to Electronic Signal Processing



Control panel of the Tektronix TDS7404 oscilloscope, featuring various knobs, buttons, and a touch screen.

HORIZONTAL POSITION: Includes buttons for AUTO, RESTORE, PRINT, GRID, and FastAcq. A large knob is used for horizontal position adjustment.

TRIGGER: Includes buttons for SOURCE, COUPLING, and SLOPE. A large knob is used for trigger level adjustment.

VERTICAL: Includes four channels (CH1, CH2, CH3, CH4) with POSITION and SCALE knobs for each. A large knob is used for vertical position adjustment.

Other controls: Includes buttons for TOUCH SCREEN, OFF, and various trigger and mode settings.

Probe compensation and signal input section of the oscilloscope.

PROBE COMPENSATION: Includes a SIGNAL input and a GND ADJUST input.

AUX IN: Auxiliary input for external signals.

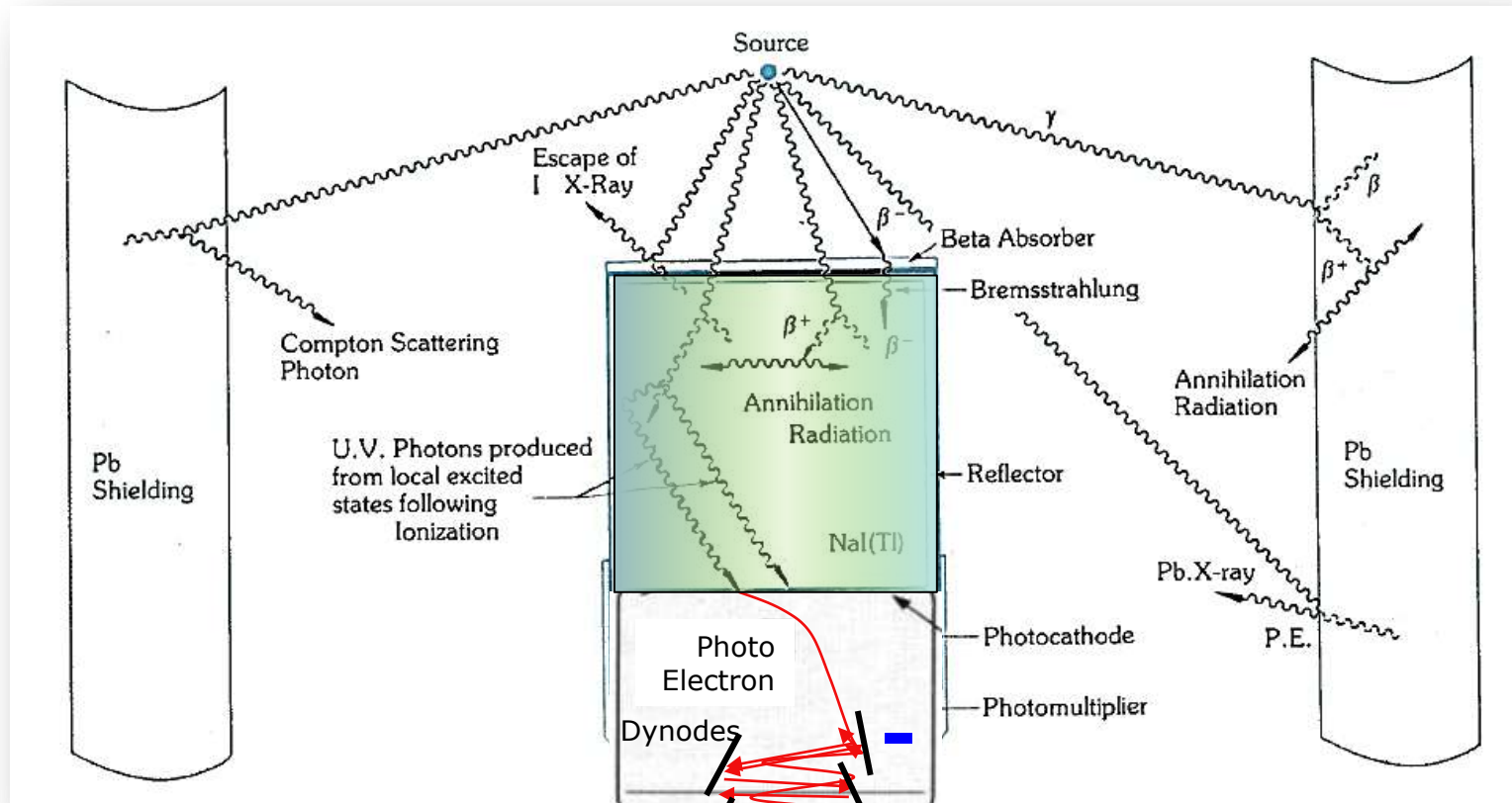
AUX OUT: Auxiliary output for external signals.

SIGNAL OUT: Signal output for external signals.

Two probe cables are connected to the SIGNAL IN and SIGNAL OUT ports.

Tektronix TCA-558A

Detector Response to Radiation

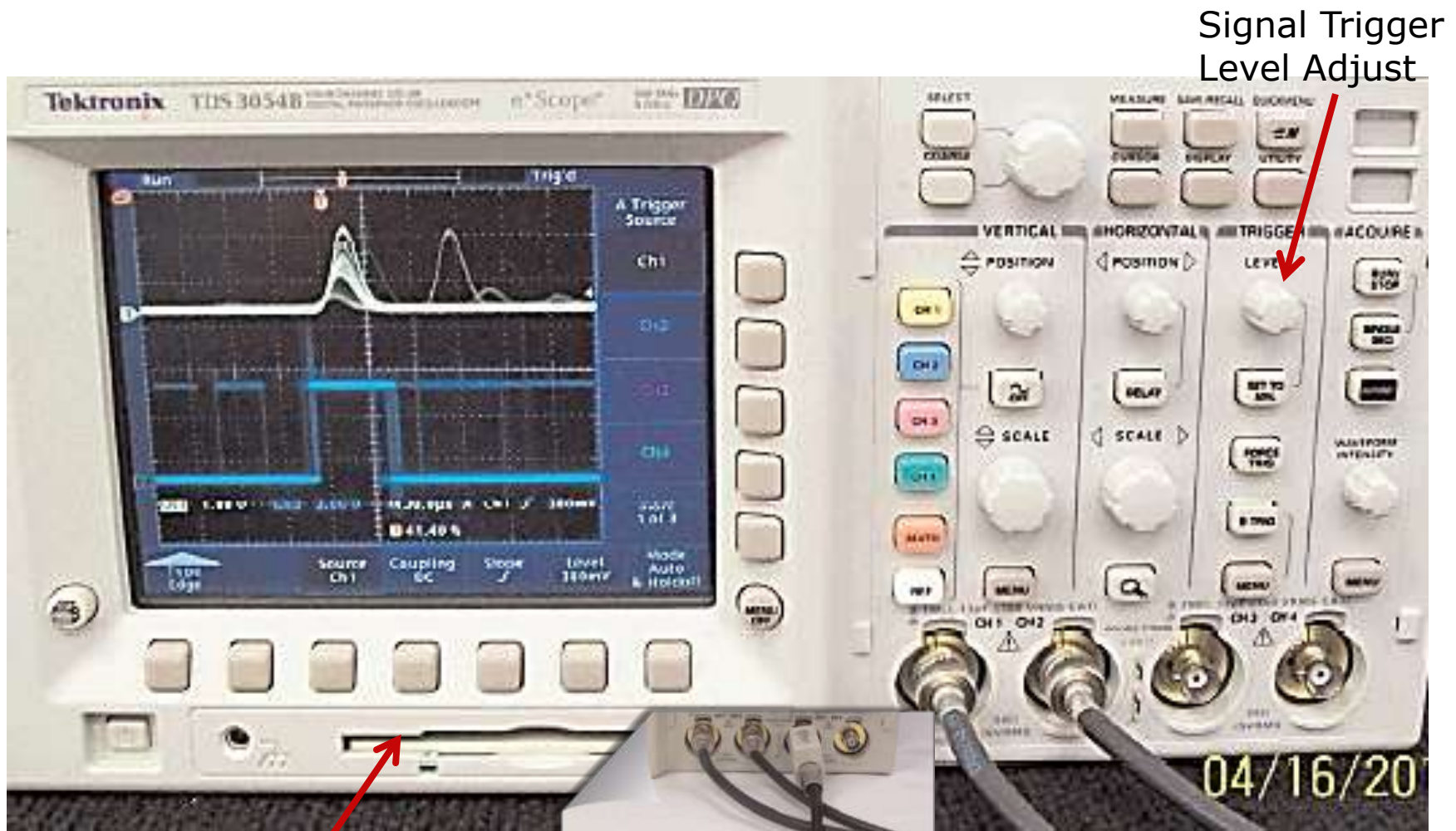


Task: Produce *at least* precision electronic measure of energy deposit in detector (via. photons, electrons), particle ID,

....
Bias/HV

Signal Processing Electronics

Digital Sampling Oscilloscope



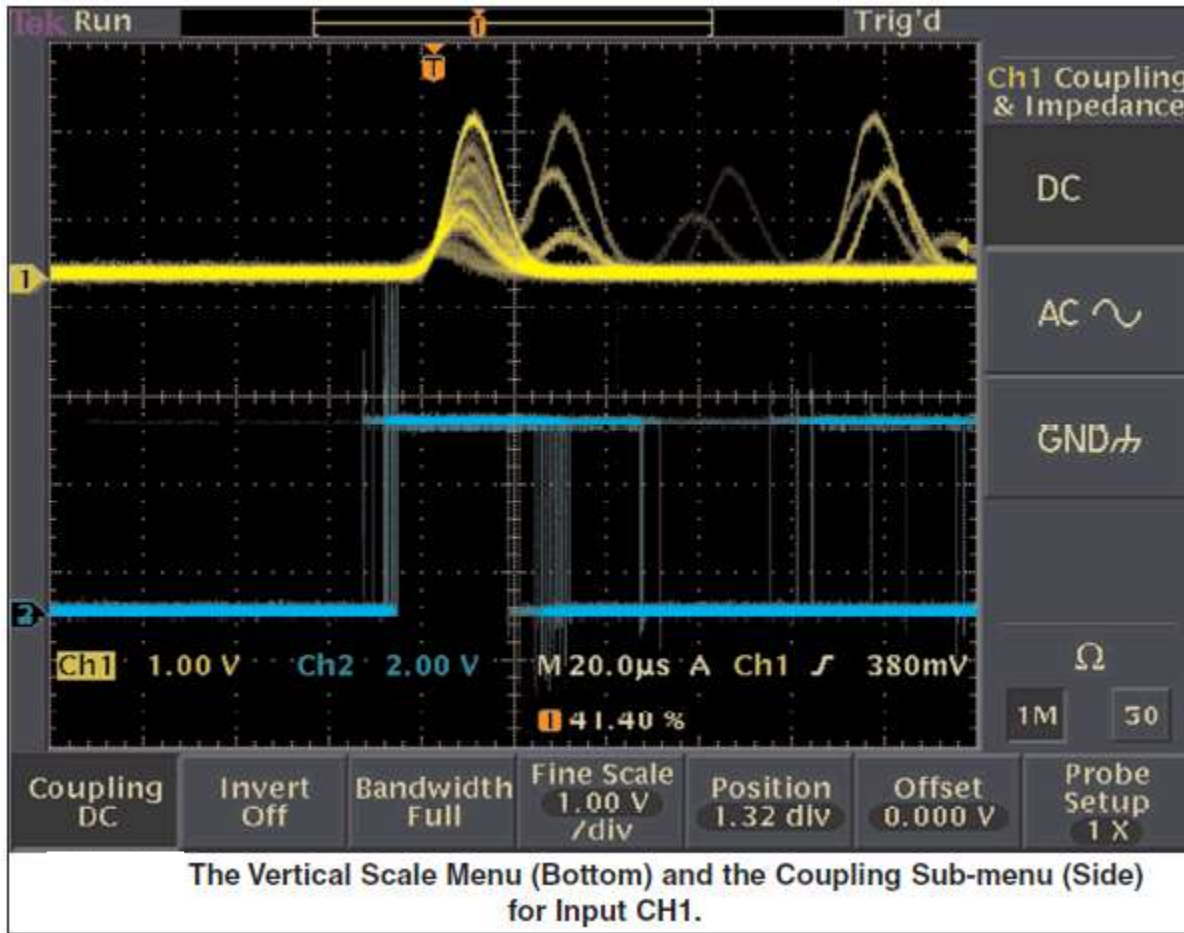
Signal Trigger Level Adjust

Output media for screen shots (USB/Disc)

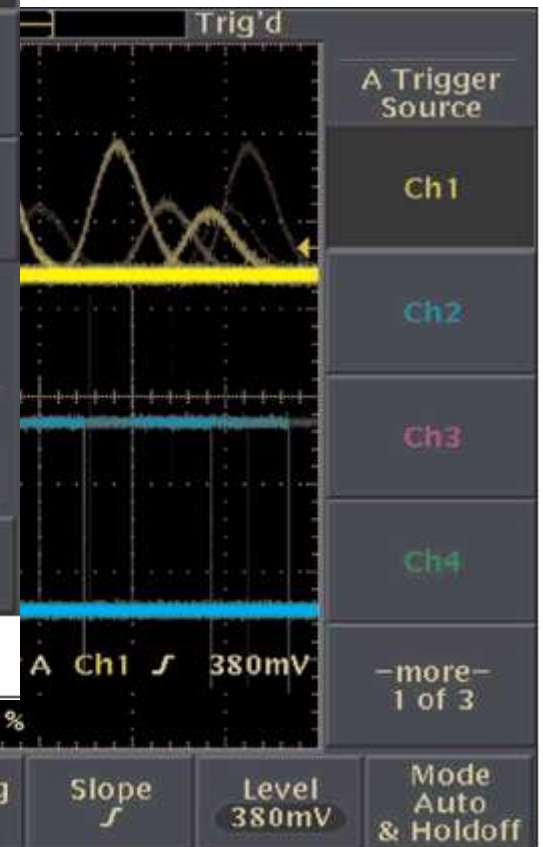


10x probe (high Ohm)

Scope Screen Shots

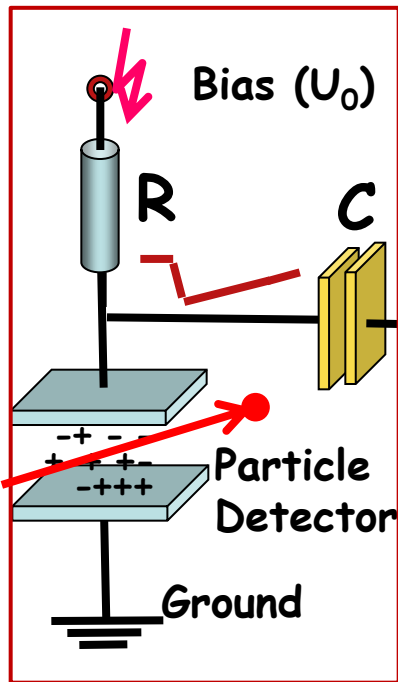


The Vertical Scale Menu (Bottom) and the Coupling Sub-menu (Side) for Input CH1.



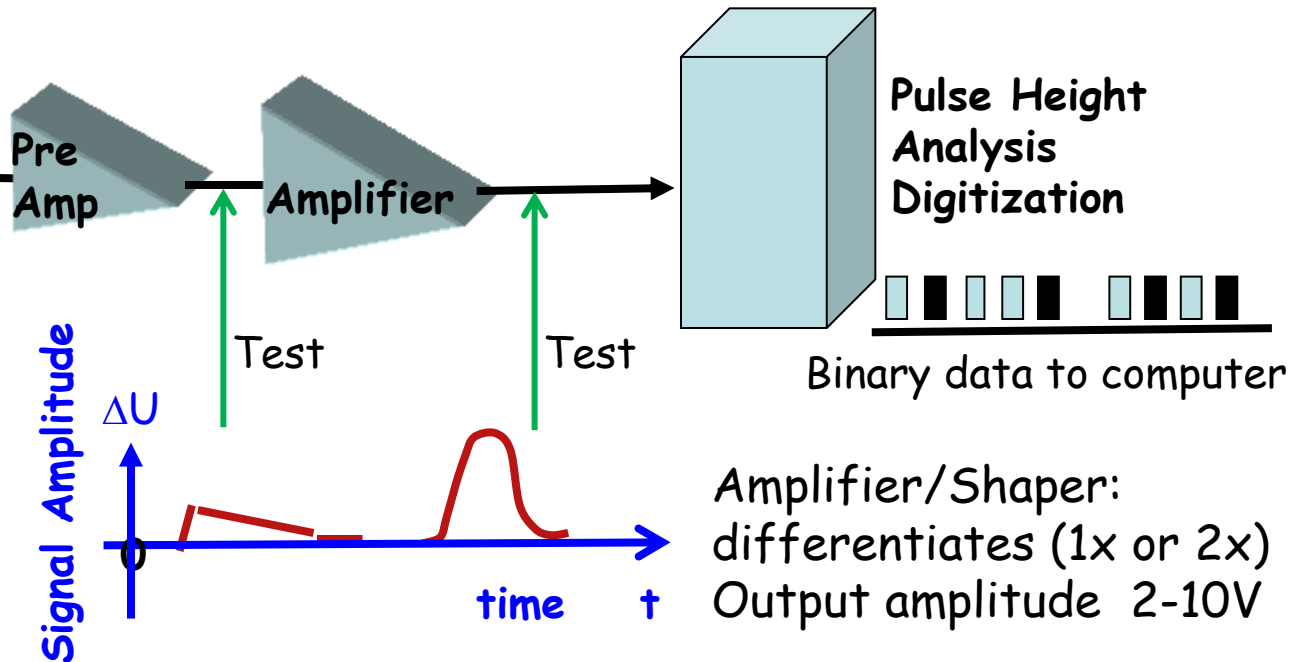
The Trigger Menu (Bottom) and Source Sub-menu (Side).

Basic Radiation Detection/Counting System



R: Load resistor
 C: Insulates electronics from HV bias.
 Pulse height 20-100 mV

Charge sensitive preamplifier: Voltage output pulse height ($\sim 0.1V$), dependent on detector and radiation.

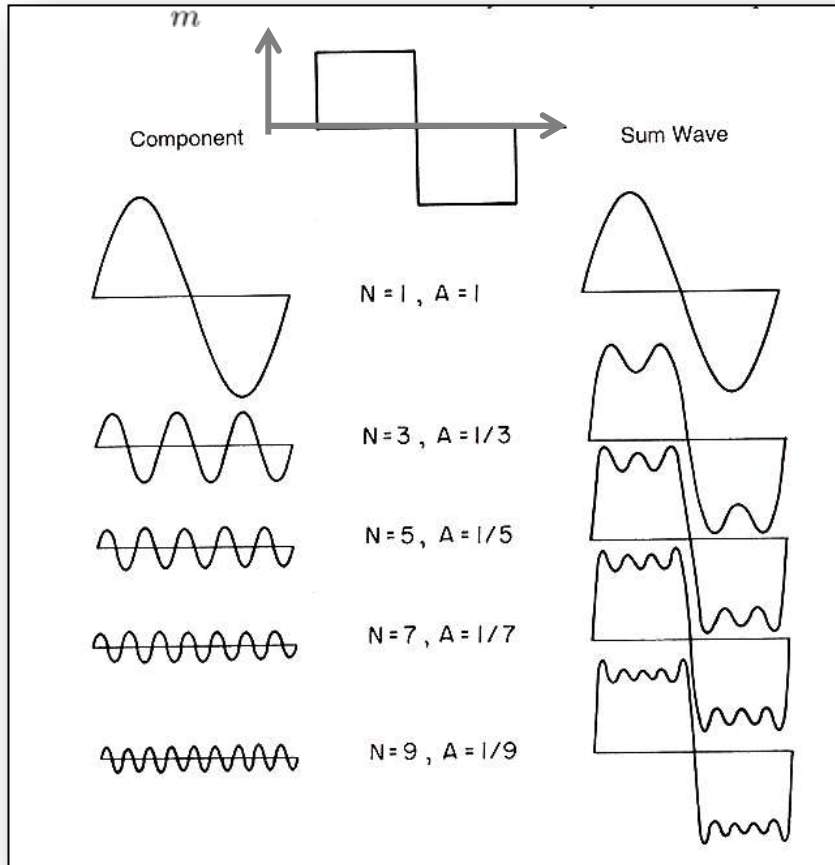


Interaction of radiation with detector temporarily ionizes medium
 → time (frequency) dependent change ΔU in electric potential at collector electrodes,
transmitted as elm. Wave $g(t) \sim f(\omega)$.

Time and Frequency Domains

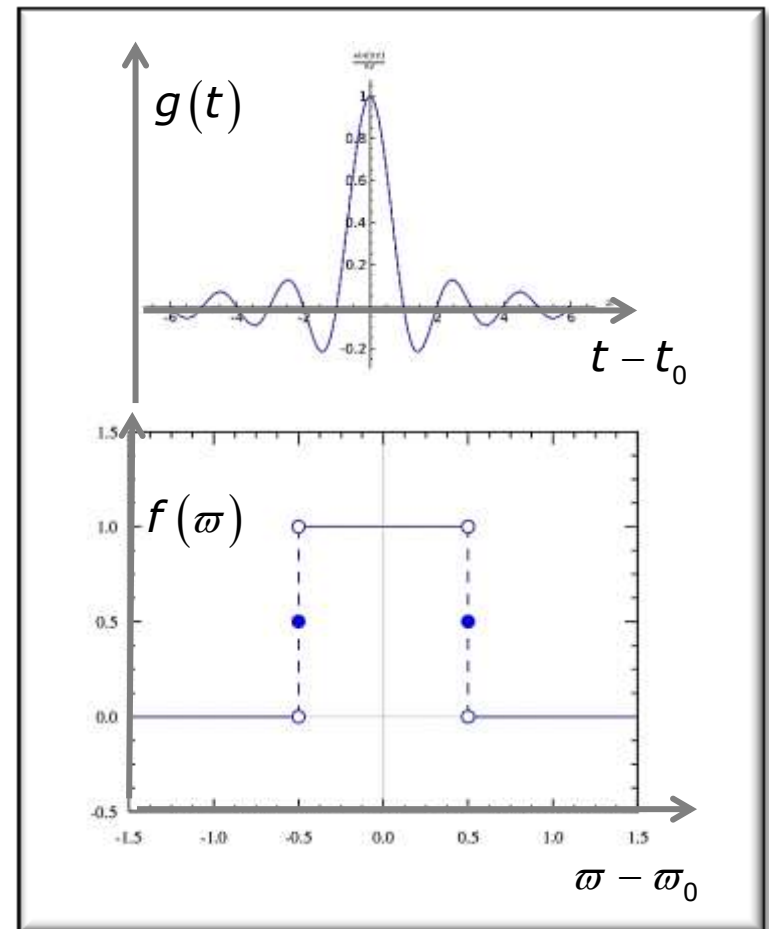
Fourier Series: adding harmonic components. Example $A_m=1/m$, $B_m=0$

$$f(t) = \sum [A_m \sin(2\pi mft) + B_m \cos(2\pi mft)]$$



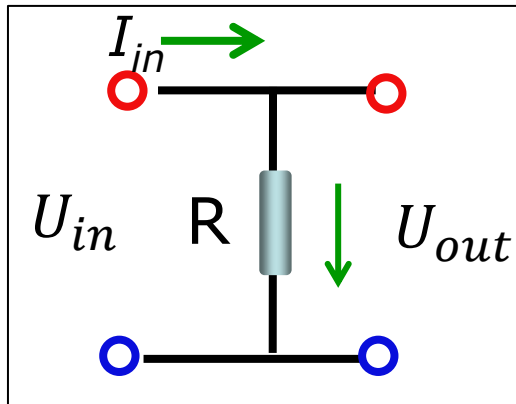
Fourier Transforms: adding continuous frequency spectrum components.

$$f(\omega) = (2\pi)^{-1/2} \int g(t) \exp[i\omega t] dt$$



Time dependent signals processed by frequency dependent electronics

Passive Electronic Circuit Components

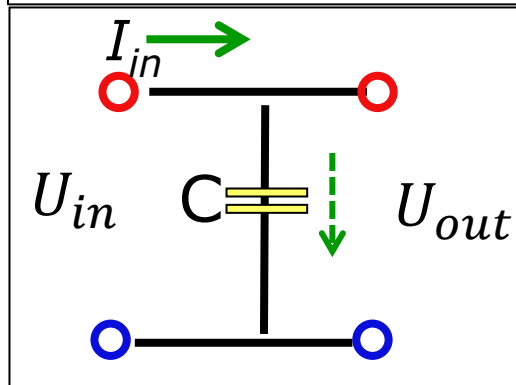


Assume no load on output ($R_{out} \rightarrow \infty$)

$$U_{out} = I_{in} \cdot R = U_{in} \rightarrow Z_R := R = U_{in} / I_{in}$$

Current flows continuously (DC or AC voltage)

$$U_{out}(t) = I_{in}(t) \cdot Z_R = U_{in}(t), \text{ Impedance } Z_R$$

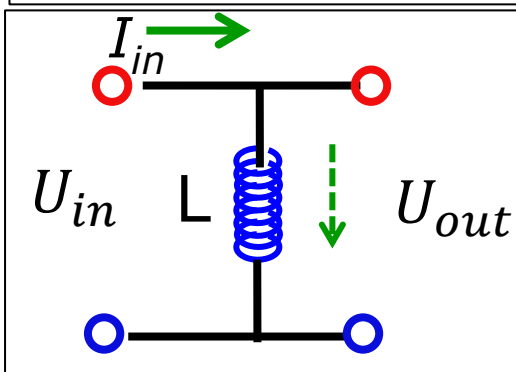


Current flows only until C is charged to $Q = C \cdot U_{in}$

$$I = \frac{dQ}{dt} = C \cdot \frac{dU_{in}}{dt} \rightarrow I = 0 \text{ for } U_{in} = \text{const}$$

$$U_{in}(t) = U_0 \cdot e^{i\omega t} \rightarrow I(t) = (i\omega C) \cdot U_{in}(t)$$

$$\text{Impedance } Z_C := U_{in}(t) / I(t) = (i\omega C)^{-1} \xrightarrow{\omega \rightarrow \infty} 0$$

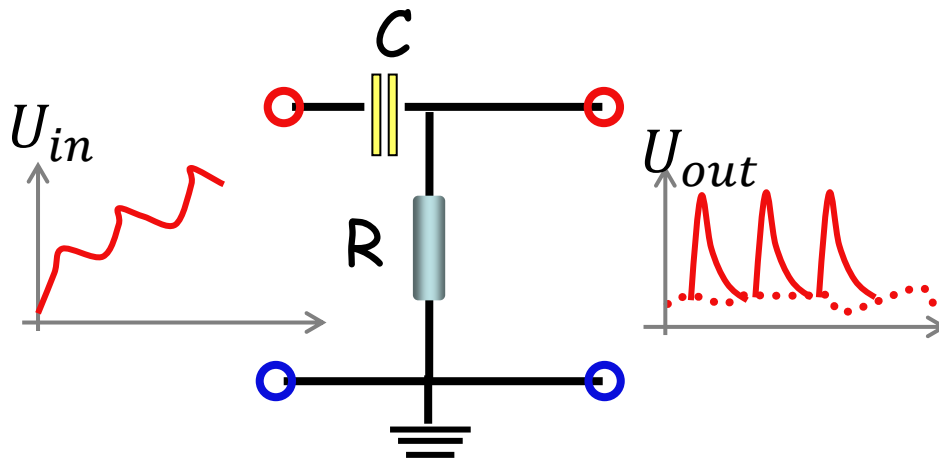


Inductance L makes a short circuit for $U_{in} = \text{const.} \rightarrow U_{out} = 0$. But for AC current/voltage

$$I_{in}(t) = I_0 \cdot e^{i\omega t} \rightarrow U_{out} = -L \frac{dI_{in}}{dt} = -(i\omega L) \cdot I_{in}(t)$$

$$\text{Impedance } Z_C := U_{in}(t) / I(t) = (-i\omega L) \xrightarrow{\omega \rightarrow \infty} \infty$$

Electronic Circuit Components

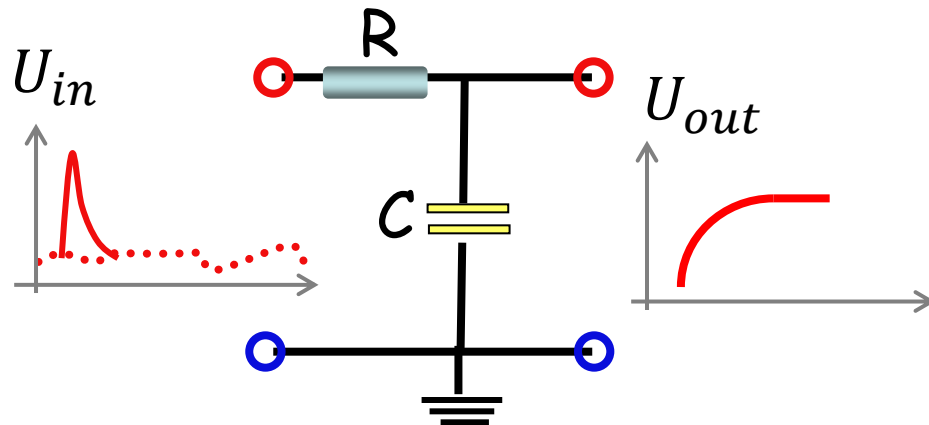


Differentiator (high pass)

$$U_{out} = \frac{R}{R + Z_C} U_{in}$$

$$\omega \rightarrow \infty: Z_C \rightarrow 0, U_{out} = U_{in}$$

Output images rapid changes of input signal $U_{out} \propto dU_{in}/dt$



Integrator (low pass)

$$U_{out} = \frac{Z_C}{R + Z_C} U_{in}$$

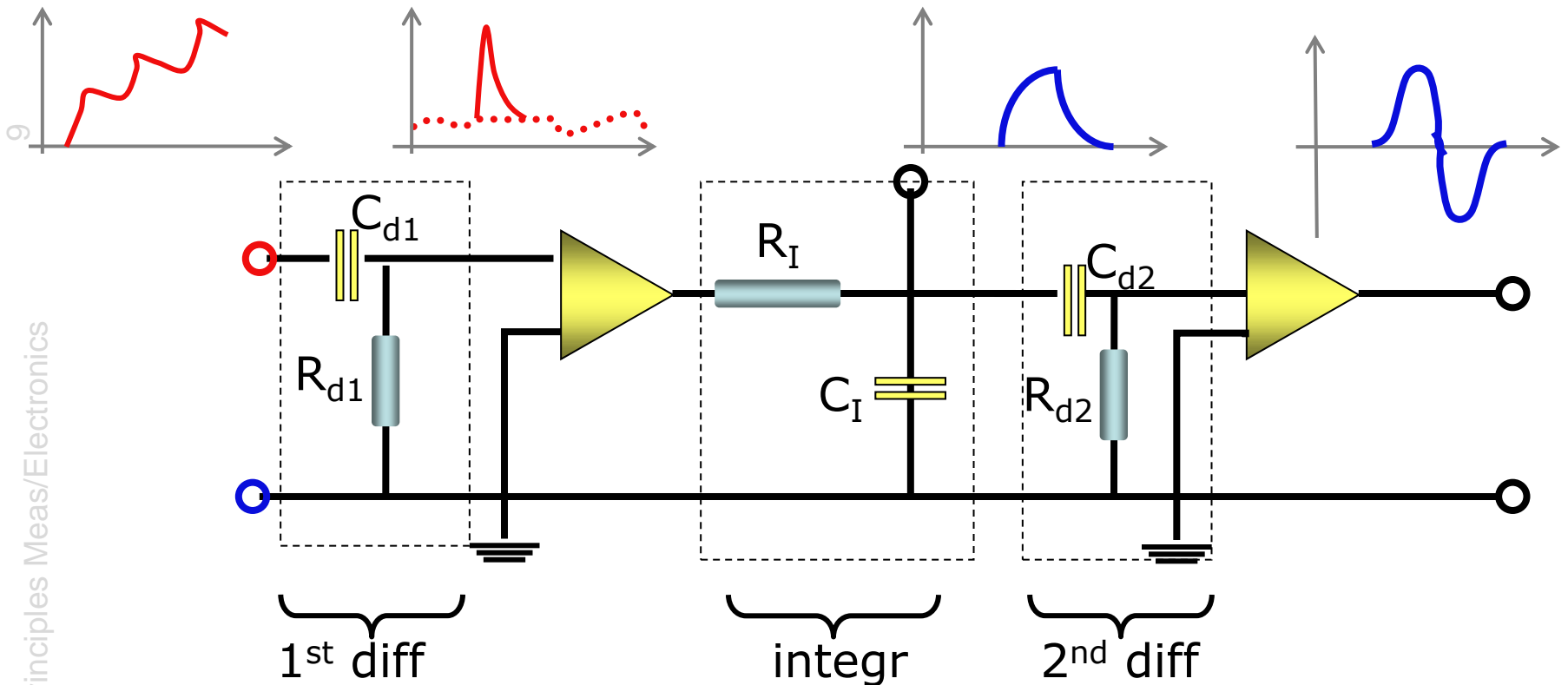
$$\omega \rightarrow 0: Z_C \rightarrow \infty, U_{out} = U_{in}$$

Filters out fast changes of input signal, smoothens input $U_{out} \propto \int U_{in} dt$

Combination of differentiators + integrators makes selective filter ("band pass")

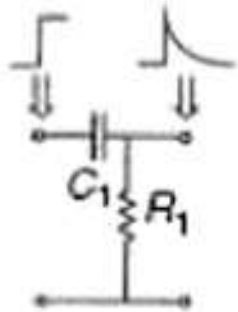
Main/Shaping Amplifiers

- Tasks:
- 1) **Linear** amplification to pulse heights of $U \approx (1-10)V$
 - 2) Improvement of signal/noise ratio (integration)
 - 3) Pulse shaping (Gaussian shape is best)

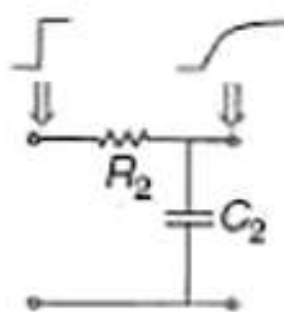


CRRC Pulse Shaping

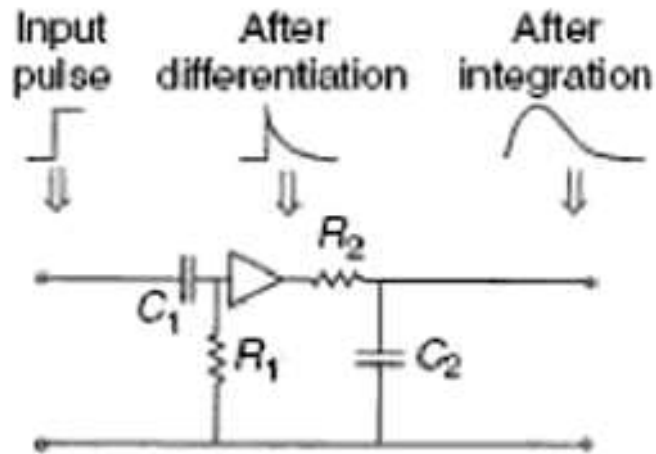
(a) Differentiation
(high-pass filter)









(b) Integration
(low-pass filter)

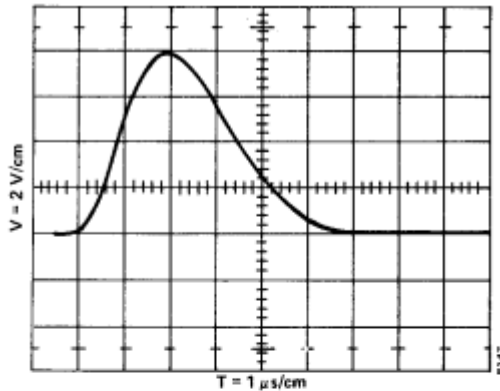


(c) Combined CRRC circuit

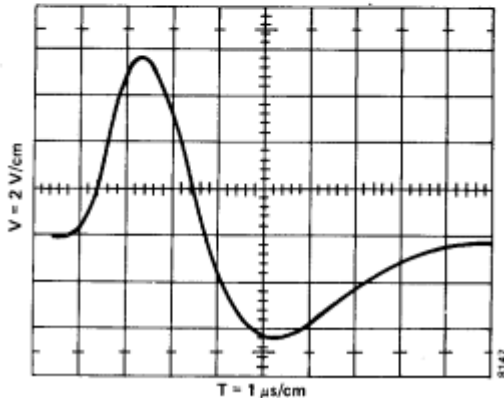


Pulse shape	Relative 'noise'
 Cusp	1.00
 Triangle	1.08
 Gaussian	1.12
 CR, nRC	$\left\{ \begin{array}{l} 1.36, n=1 \\ 1.22, n=2 \\ 1.18, n=3 \\ 1.12, n=\infty \end{array} \right.$
 Double CR, RC	1.88
 Delay line DL, RC	1.10–1.41

Main/Spectroscopy Amplifiers



Correct Amplifier Unipolar Output.



Correct Amplifier Bipolar Output.

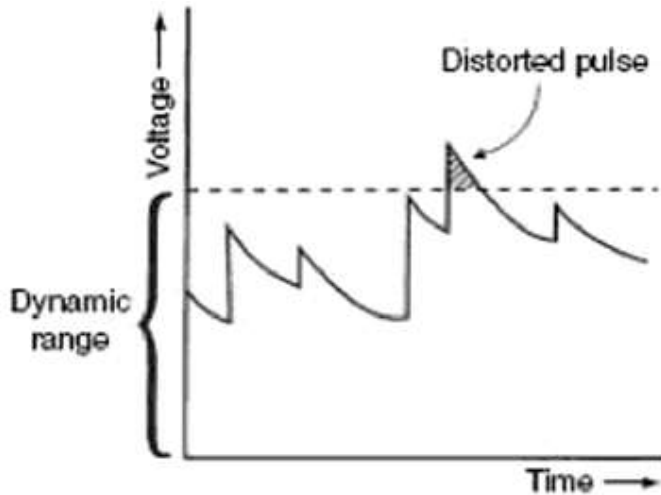


Tasks: Generate signal with amplitude **proportional** to collected detector charge. Needs absolute calibration of pulse amplitude.

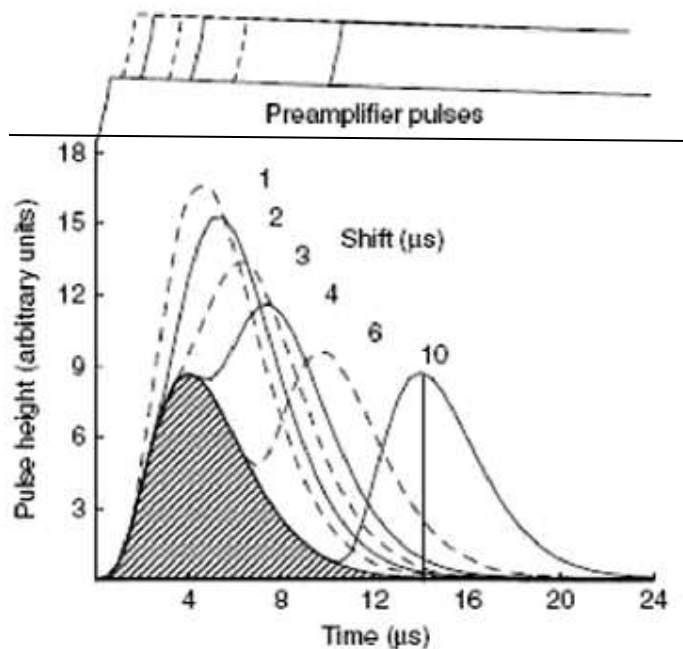


← **Preamp Power**

Effects of Pile-Up



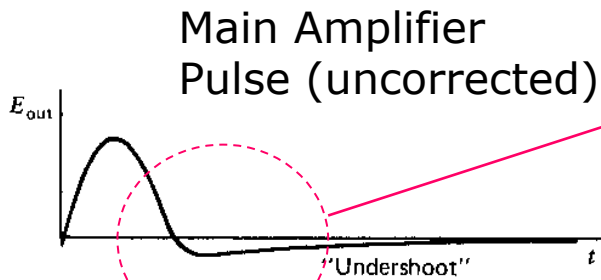
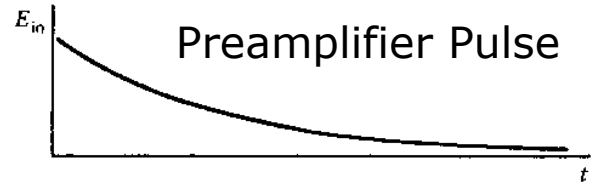
High count rate (relative to pulse length/decay time) can lead to pile up
→ from small non-linearities to serious distortions, line shapes "ghost lines"



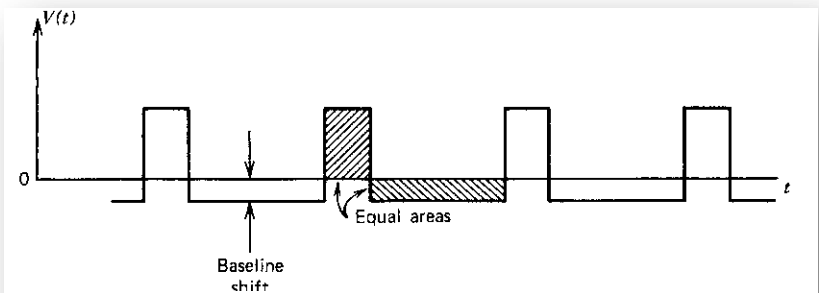
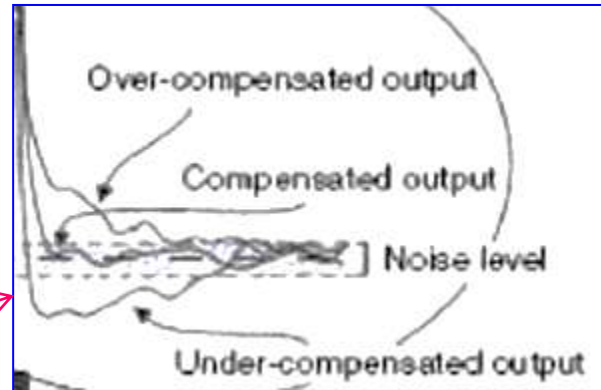
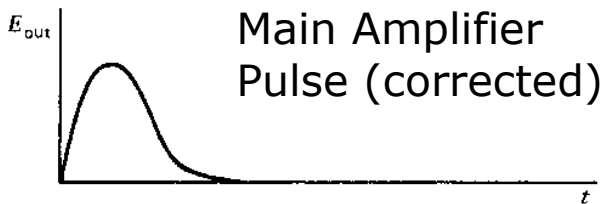
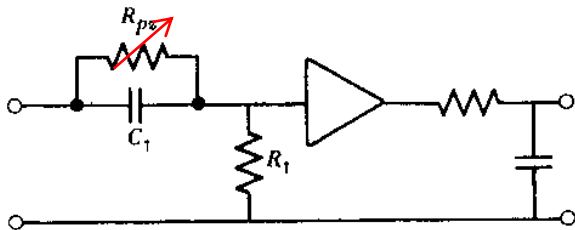
Artificial test of the pile-up effect. Successive signals add on to each other, creating an effectively non-zero base line.

Avoid by reducing signal rate or width (pulse decay time)

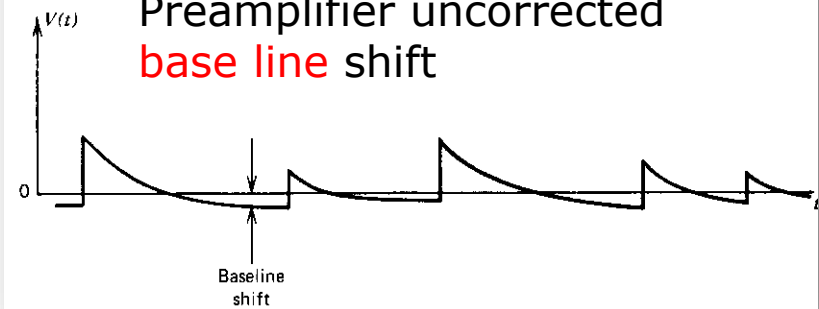
Pole-Zero & Base Line Shift Distortions



Pole-Zero adjust



Preamplifier uncorrected
base line shift



Test Modules: Precision Pulse Generators



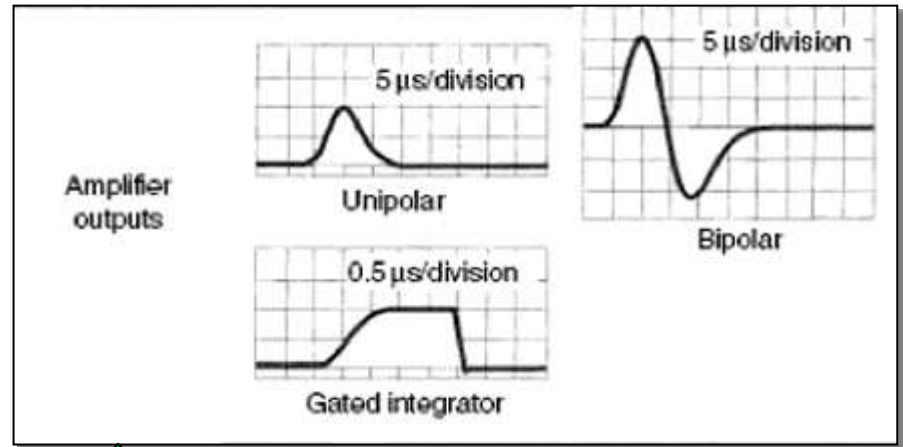
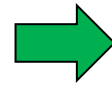
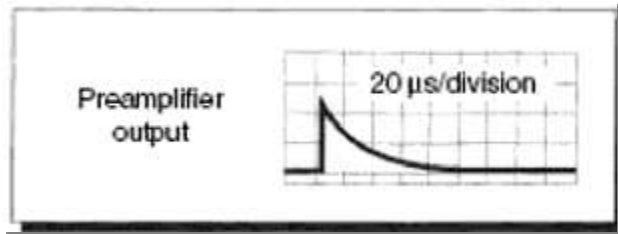
Tasks:

Simulate signals of physical events, controlled pulse shape, amplitude, timing, repetition rate

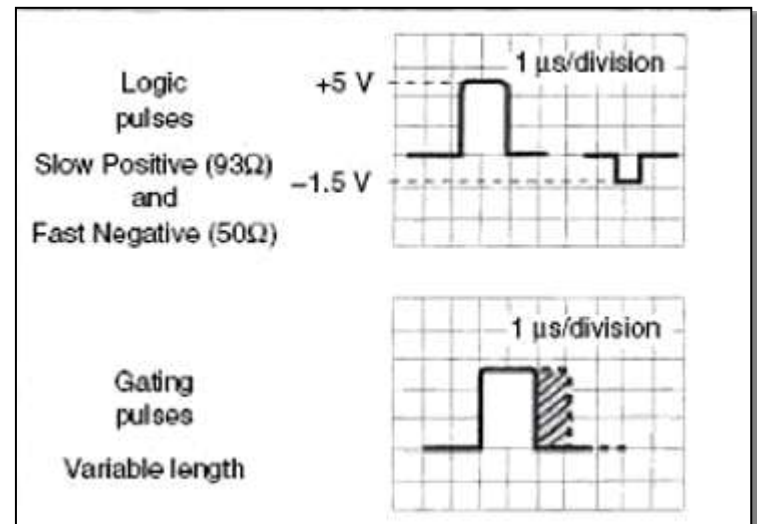
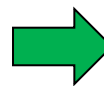
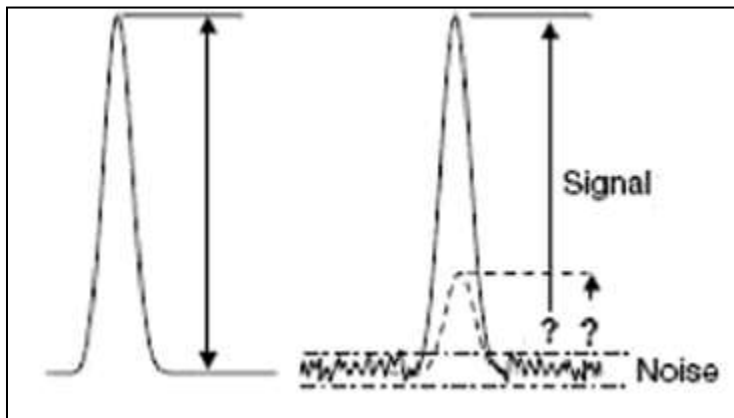
→ Adjust analog and logic electronics

Analog Plus Digital Circuitry

Analog (slow) circuit → proportional image detector output signal

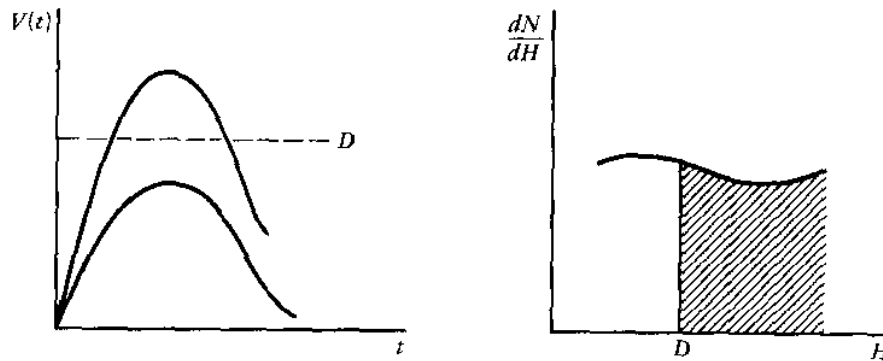


Digital (fast) circuit → yes/no information on signal presence

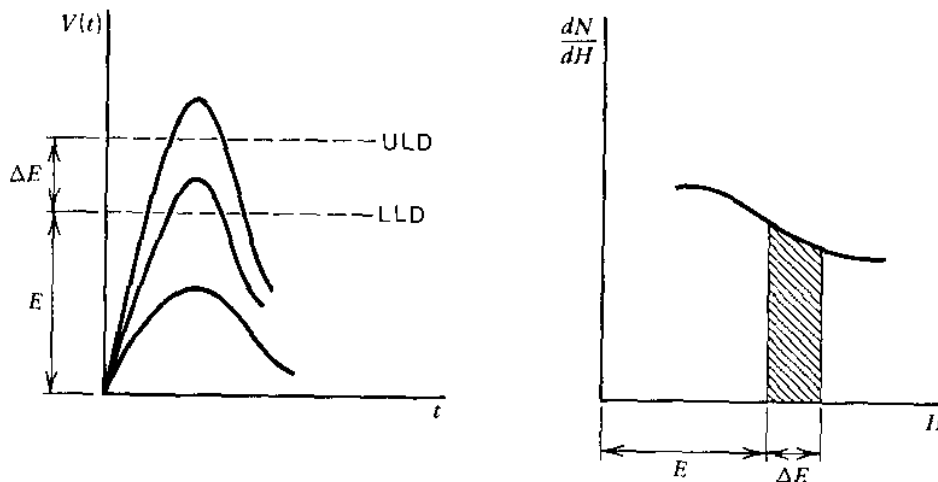


Single Channel Analyzer

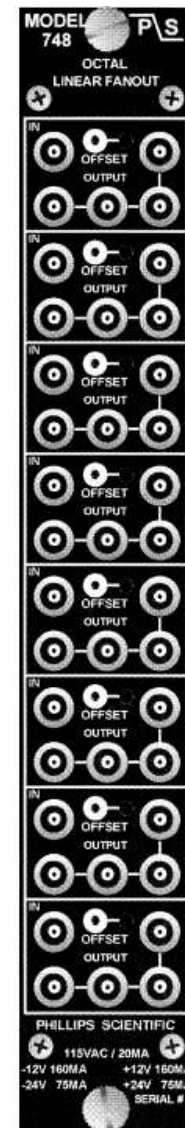
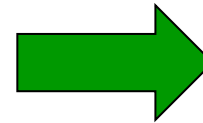
(a) Integral discriminator



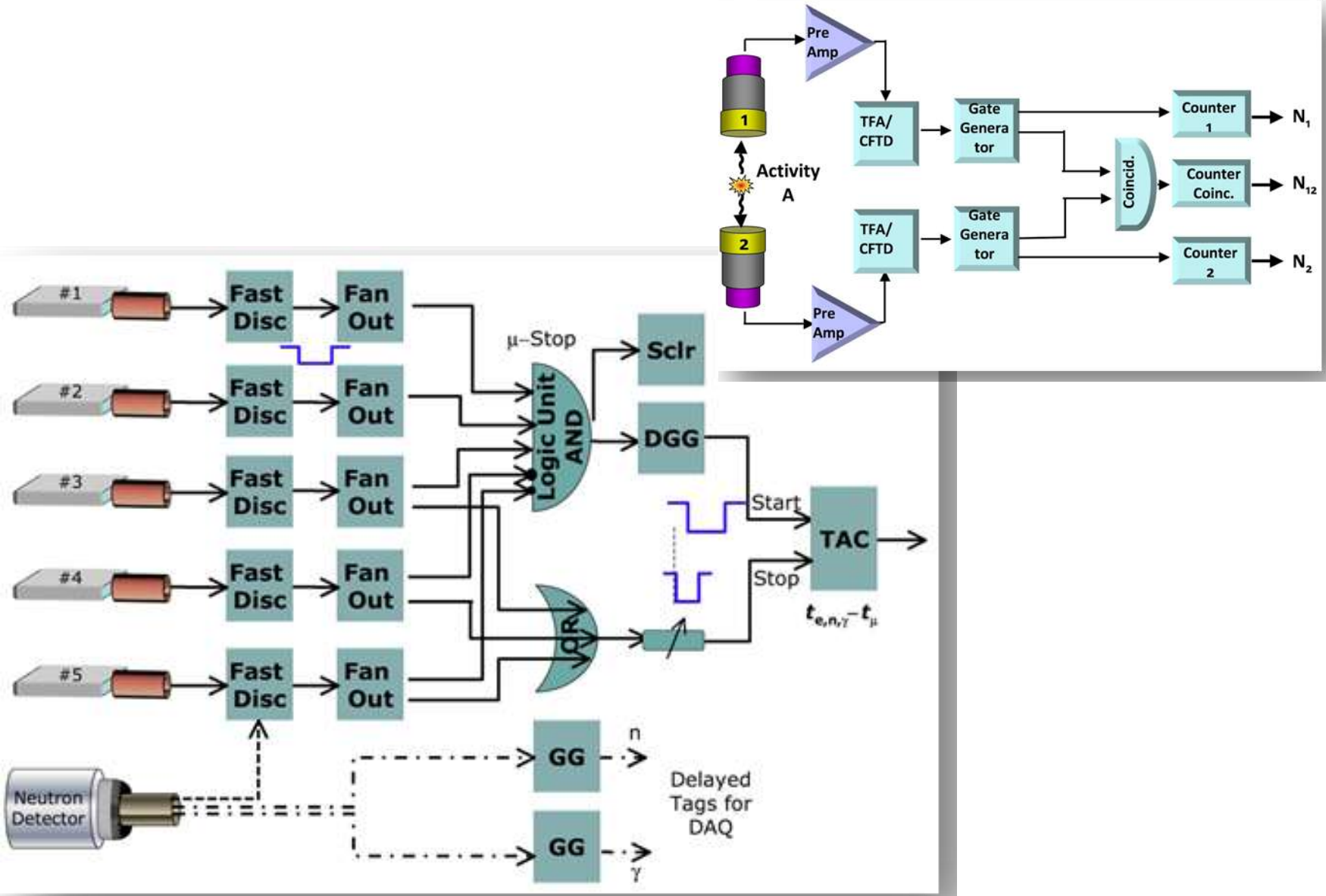
(b) Differential discriminator (single-channel analyzer)



Digital Pulse Shaping: Gate and Delay Generators

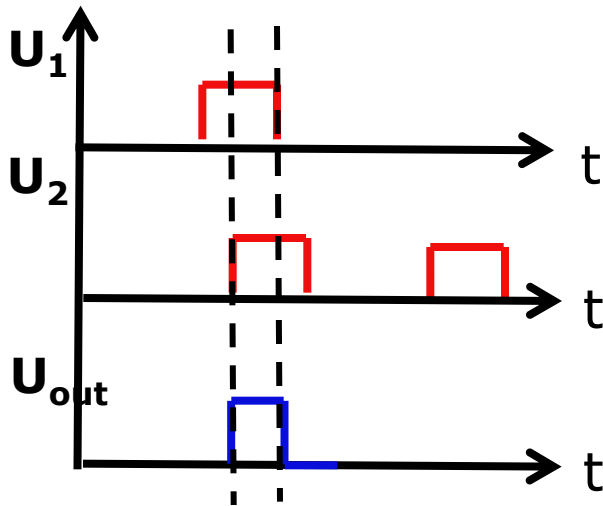
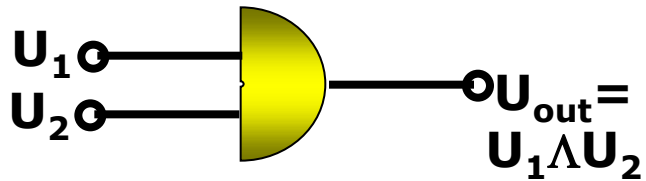


Digital Logic Circuits

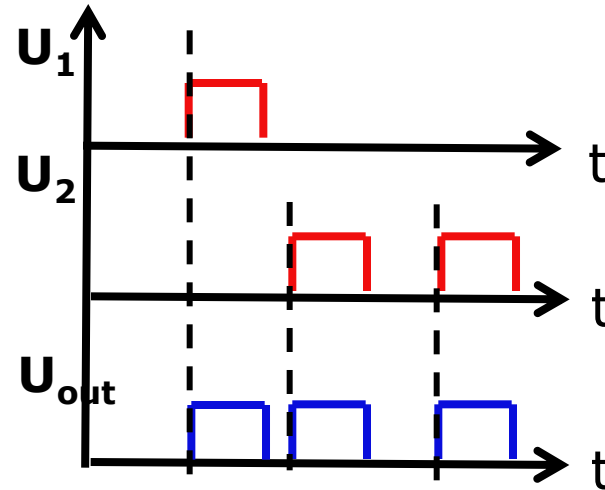
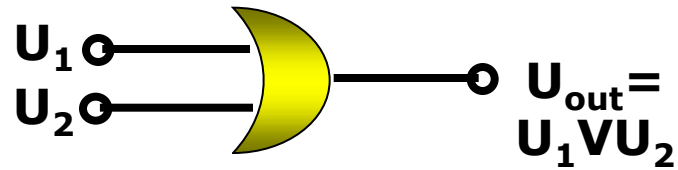


Logic Modules

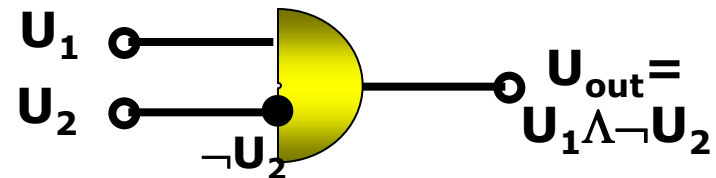
Overlap Coincidence



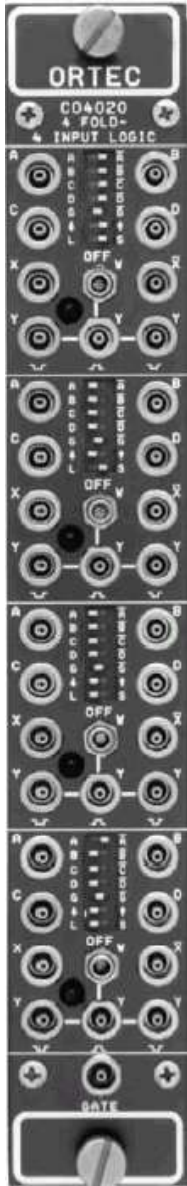
Or (inclusive)



For fast timing: use fast negative logic



Anti-Coincidence



Input Logic Switches (A/OFF/A↓ , B/OFF/B↓ , C/OFF/C↓ , D/OFF/D↓ , AND G/OFF/G↓) As defined in Fig. 1, these switches select variations of the following basic logic functions.

In the OFF position, the state of that input is ignored. With switches set to the A, B, C, D, and G positions, the module performs the OR function at the X↓ output.

In the OFF position, the state of that input is ignored. With switches set to the A, B, C, D, and G positions, the module performs the OR function at the X↓ output.

$$X = A + B + C + D + G$$

Setting the switches to the A↓ , B↓ , C↓ , D↓ , and G↓ positions provide the AND (coincidence) function at the X↓ output.

Setting the switches to the A↓ , B↓ , C↓ , D↓ , and G↓ positions provide the AND (coincidence) function at the X↓ output.

$$X \downarrow = A \cdot B \cdot C \cdot D \cdot G$$

Changing the G↓ switch to G implements the common-gate veto (anticoincidence).

$$X \downarrow = A \cdot B \cdot C \cdot D \cdot G \downarrow$$

See Fig. 1 to determine other possible logic

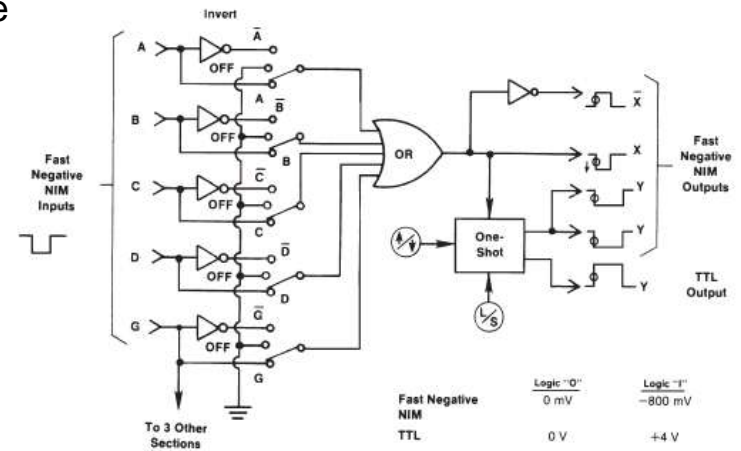
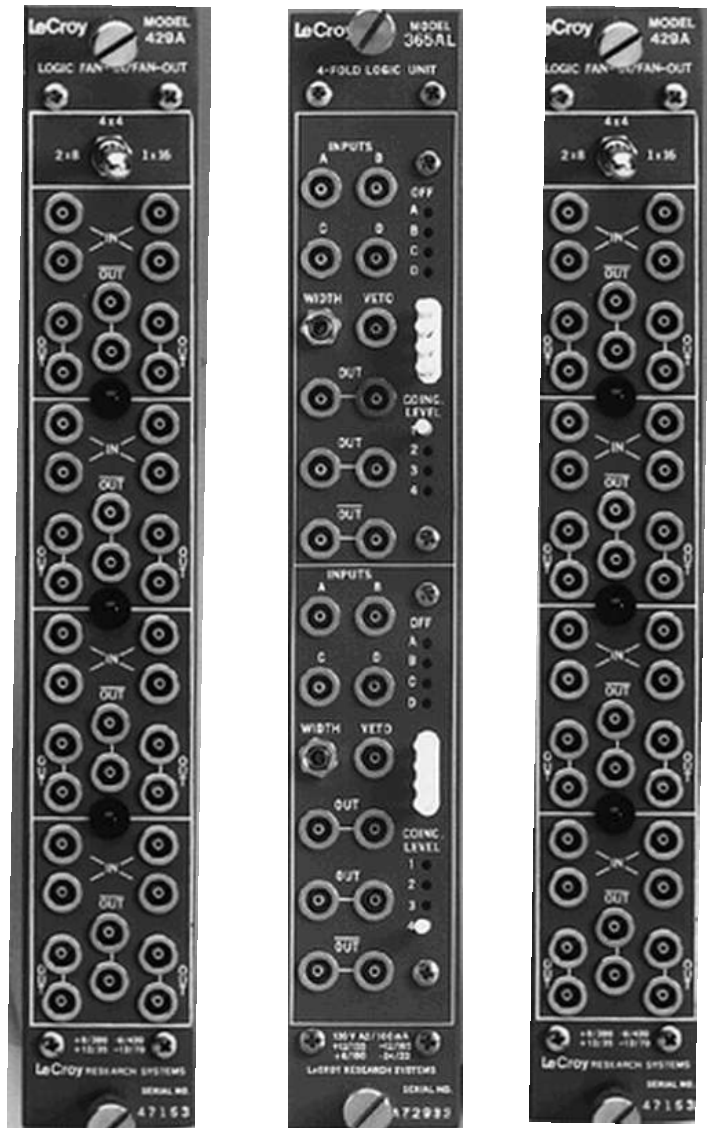
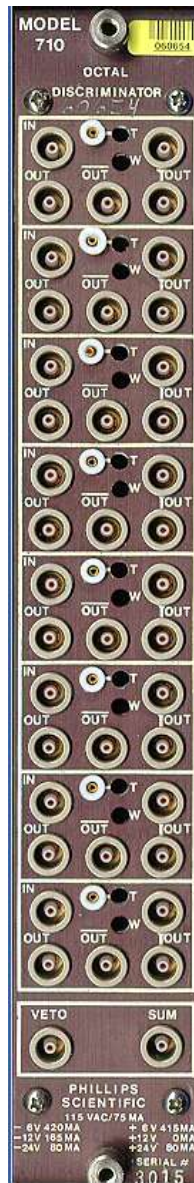
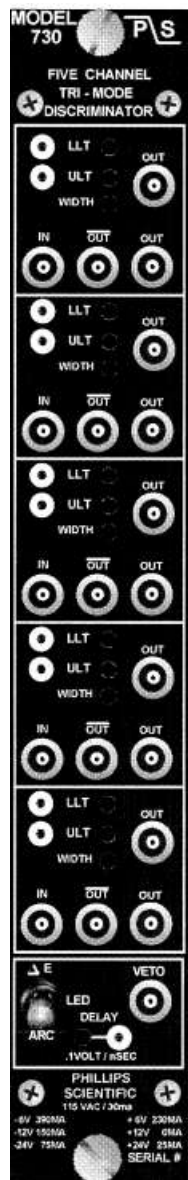
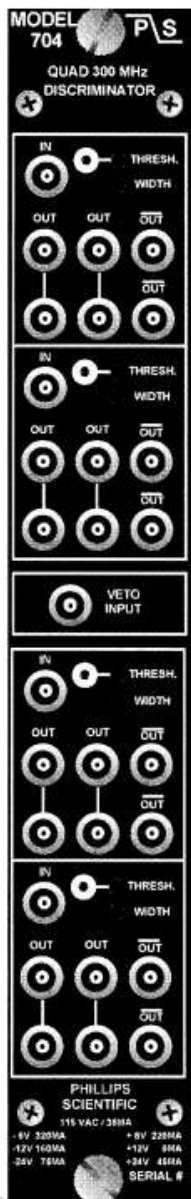
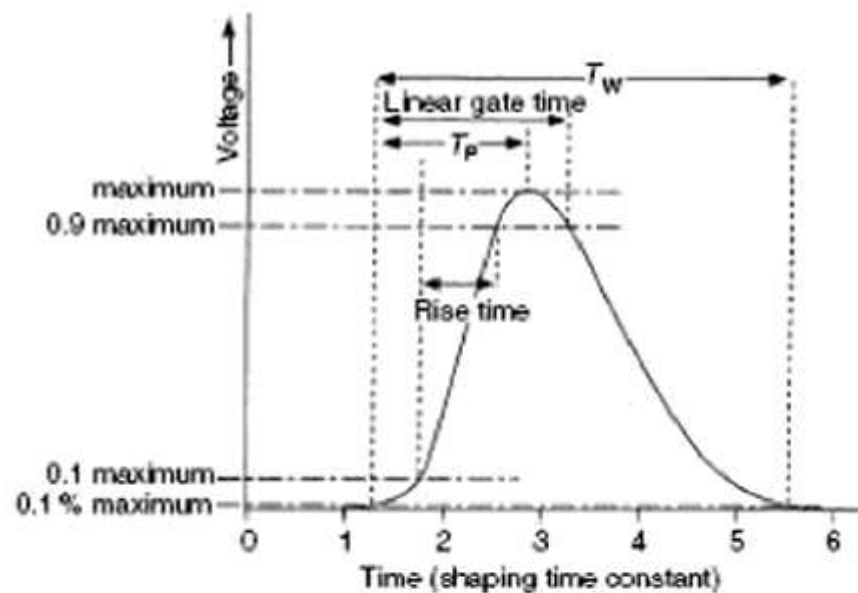


Fig. 1. Block Diagram of the Model CO4020 Logic Unit.



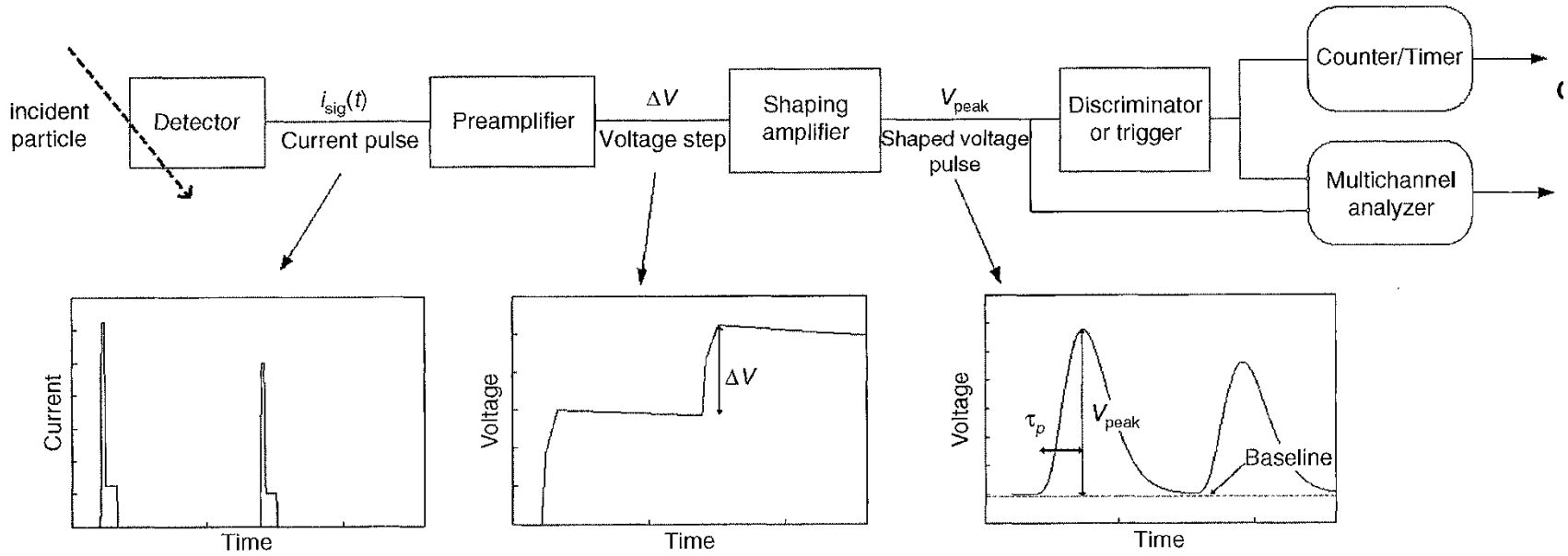


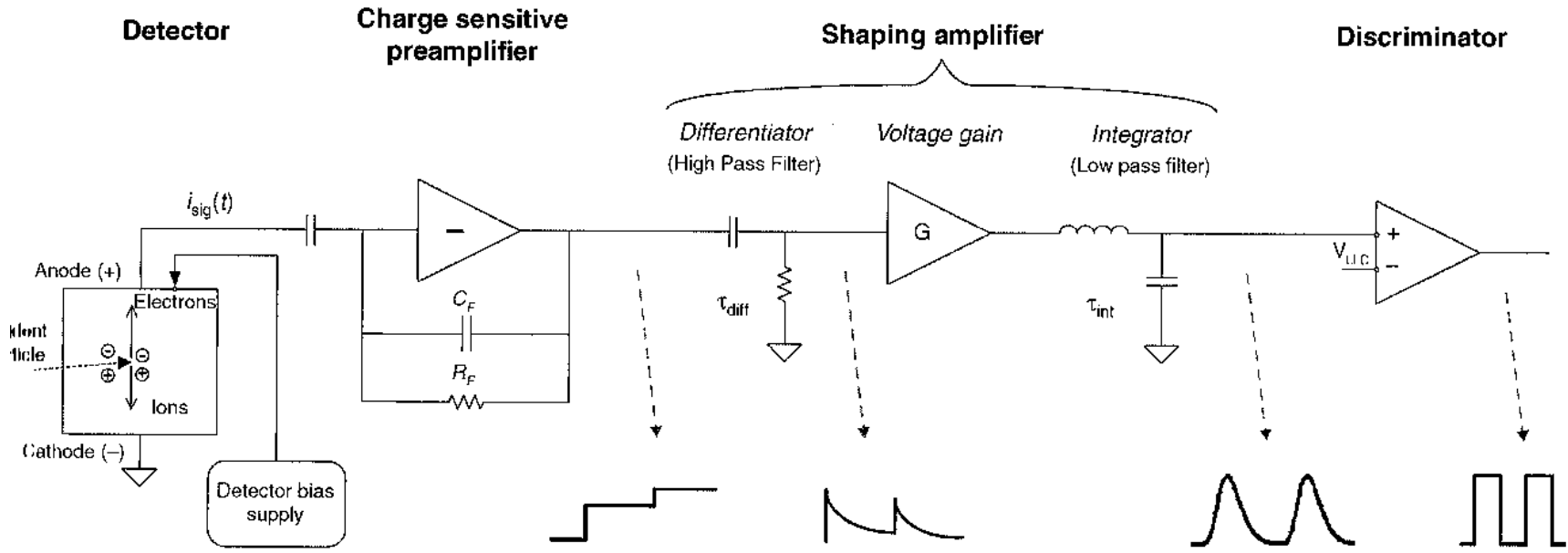


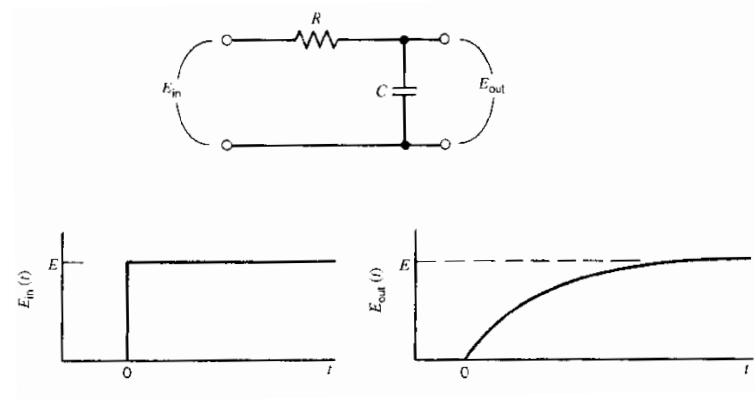
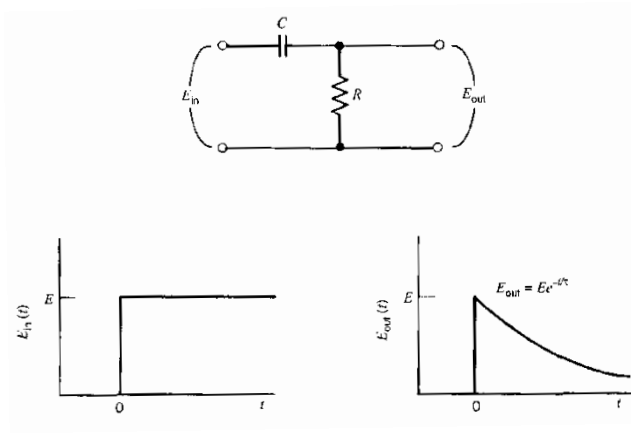


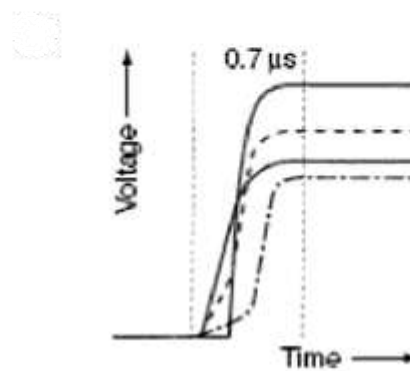
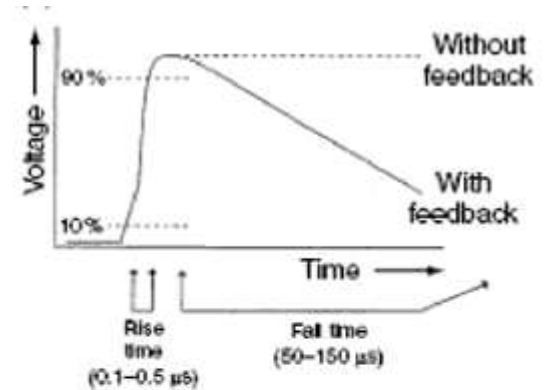
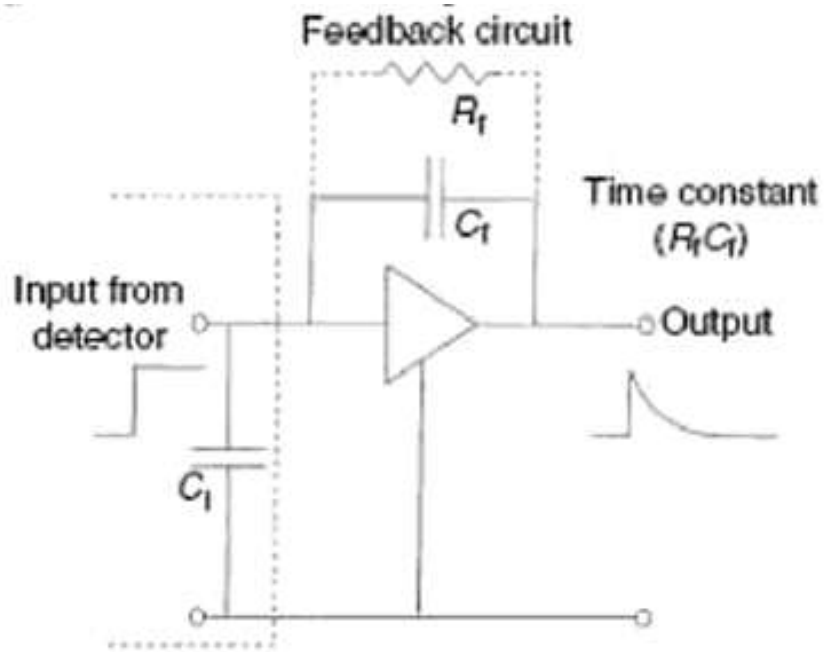
Measured timing factors for semi-Gaussian output pulses

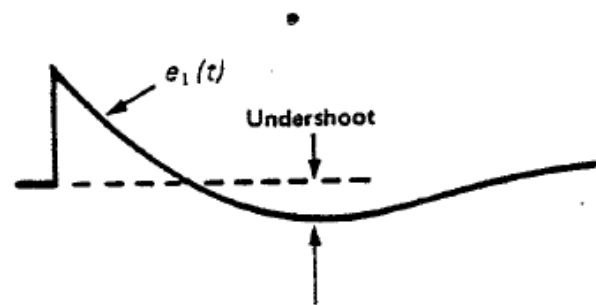
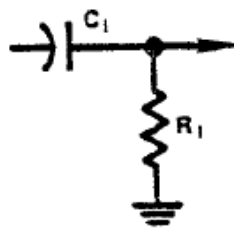
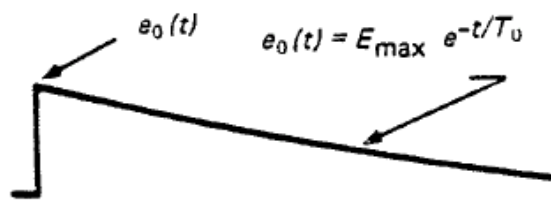
Factor	Time interval	Symbol	Time ^a
Rise time	0.1 to 0.9 of pulse maximum	—	$1.26 + 0.05$
Peaking time	threshold ^b to maximum	T_p	$2.1 + 0.1$
Linear gate time	threshold to 0.9 of max. beyond max.	T_{LG}	$2.6 + 0.2$
Width	threshold to threshold	T_w	$5.6 + 0.5$











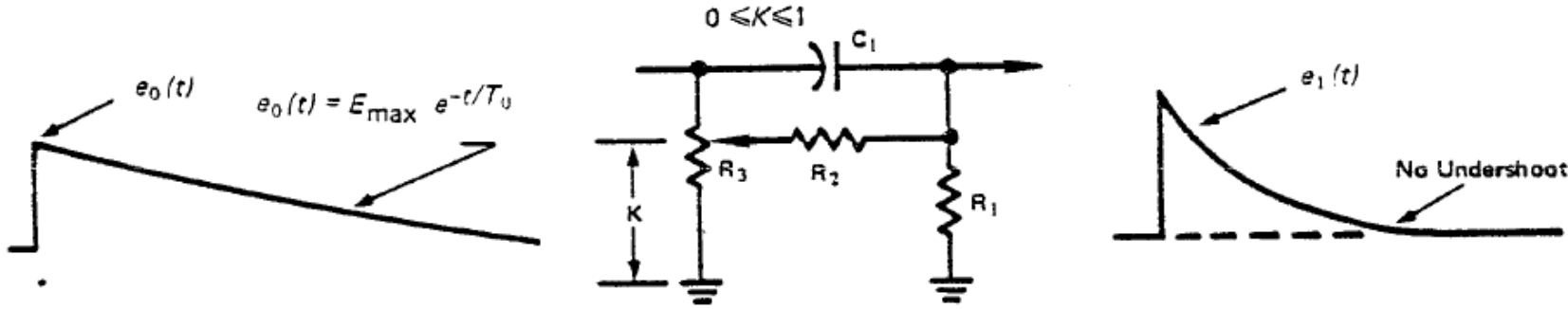
Charge loop output \times First differentiate network = Differentiated pulse with undershoot

$$E_{\max} e^{-t/T_0} \times G(t) = e_1(t).$$

$$E_{\max} \frac{1}{s + \frac{1}{T_0}} \times \frac{s}{s + \frac{1}{R_1 C_1}} = E_1(s) \text{ (Laplace transform).}$$

$$\frac{E_{\max}}{T_0 - T_1} T_0 e^{-t/T_0} - T_1 e^{-t/T_0} = e_1(t), \text{ where } T_1 = R_1 C_1.$$

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Charge loop output \times Pole-zero cancelled differentiate network = Differentiated pulse without undershoot

$$E_{\max} e^{-t/T_0} \times G(t) = e_1(t).$$

$$E_{\max} \frac{1}{s + \frac{1}{T_0}} \times \frac{s + \frac{K}{R_2 C_1}}{s + \frac{R_1 + R_2}{R_1 R_2 C_1}} = E_1(s). \text{ (Laplace transform).}$$

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Pole zero cancel by letting

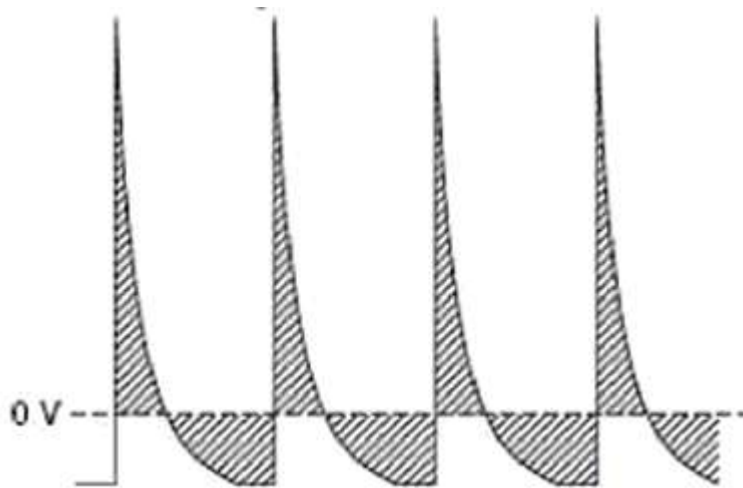
$$s + \frac{1}{T_0} = s + \frac{K}{R_2 C_1}.$$

or

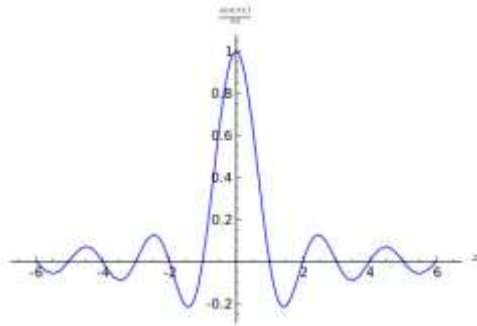
$$\frac{E_{\max}}{s + \frac{R_1 + R_2}{R_1 R_2 C_1}} = \frac{E_{\max}}{s + \frac{1}{R_p C_1}} = E_1(s), \text{ where } R_p = \frac{R_1 R_2}{R_1 + R_2}.$$

$$E_{\max} e^{-t/R_p C_1} = e_1(t).$$

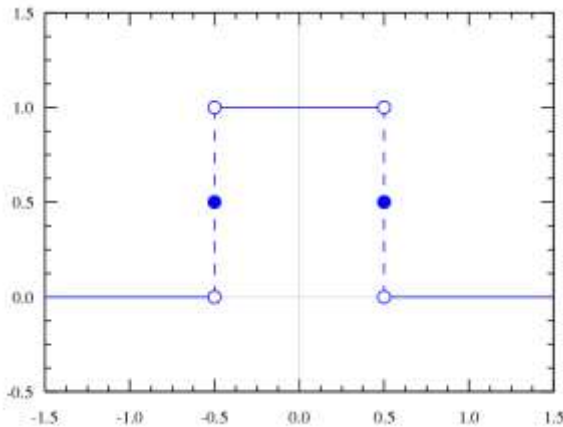
Fig. 1.2. Differentiation in a Pole-Zero Cancelled Amplifier.



Time Dependent Electronic Signals



Function	Fourier transform unitary, ordinary frequency	Fourier transform unitary, angular frequency
$f(x)$	$\hat{f}(\xi) = \int_{-\infty}^{\infty} f(x) e^{-2\pi i x \xi} dx$	$\hat{f}(\omega) = \frac{1}{\sqrt{2\pi}} \int_{-\infty}^{\infty} f(x) e^{-i\omega x} dx$



$$f(\omega) = \frac{1}{\sqrt{2\pi}} \int_{-\infty}^{+\infty} g(t) e^{i\omega t} dt$$

$$\hat{f}(\omega) = \int_{\mathbb{R}^n} f(x) e^{-i\omega x} dx.$$

Under this convention, the inverse transform becomes:

$$f(x) = \frac{1}{(2\pi)^n} \int_{\mathbb{R}^n} \hat{f}(\omega) e^{i\omega x} d\omega.$$

